
Smartlaunch 4.1 (Cracked) Internet Cafe Suite



In order to fix your problem, please close all the programs or simply open the Internet Explorer and then type address bar and type "Mozilla Firefox" in the address bar. You can change the view in your home page. 1.Double-click Start button and select Run.2.In the Open box, type explorer and click OK. You can change the view in your home page.

1.Go to Start button and select Run. 2.In the Open box, type explorer and click OK. If you are a beginner, follow these steps to solve the virus problem. 1.Open Internet Explorer. 2.Click the Tools button then click Internet Options. 3.Click the General tab. 4.Click Delete. 5.Press Ctrl + F5. 6.Click the OK

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Card Server.1. Field of the Invention
The present invention relates to a
semiconductor device having an SOI
structure and, more particularly, to a
semiconductor device having a SOI
structure that prevents generation of leak
current. 2. Description of the Related
Art A conventional semiconductor
device having an SOI structure is
disclosed in, for example, Unexamined
Japanese Patent Application KOKAI
Publication No. 2005-521355. This
semiconductor device has a structure in
which a substrate has an SOI structure
that includes a substrate and a buried

insulating layer provided on the surface of the substrate. This semiconductor device also has a MOS transistor that includes a gate insulating film provided on the buried insulating layer, a gate electrode provided on the gate insulating film, and source/drain regions provided on both sides of the gate electrode in the substrate. A conventional semiconductor device having such a SOI structure is arranged to prevent gate-induced drain leakage (GIDL) of drain-side region by providing an offset region on a substrate and increasing the distance between the drain region and the offset region. However, the above conventional semiconductor device has a problem that

it is not desirable to provide an offset region on a substrate in view of manufacturing process, consumption of manufacturing cost, etc. The present invention is devised to provide a semiconductor device having an SOI structure that prevents generation of leak current without providing an offset region on a substrate. Q: How to create a Maven artifact

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